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PTO/SB/97 (08-04)

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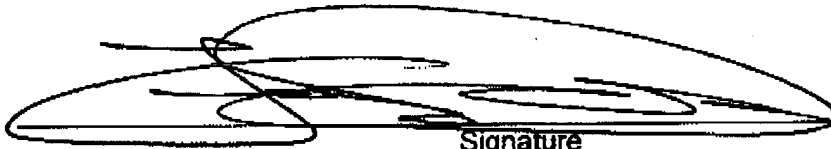
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Applicants: Moghissi, et al.

Serial No.: 10/824,061

Filing Date: 04/14/2004

Docket No.: CCT 106

Title: VOLTAGE MEASUREMENT WITH AUTOMATED CORRECTION FOR INPUT IMPEDANCE ERRORS

Appended is an Information Disclosure Statement By Applicant (PTO/SB/08a (08-03)) for filing in the above-identified patent application.

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

1

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### Complete If Known

Application Number	10/824,061
Filing Date	04/14/2004
First Named Inventor	Moghissi, et al.
Art Unit	2858
Examiner Name	(Not yet assigned)
Attorney Docket Number	CCT 108

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner  
Signature**

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinda Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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